

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on) max}$	$I_D$ $T_A = +25^\circ C$
60V	140mΩ @ $V_{GS} = 10V$	2.3A
	170mΩ @ $V_{GS} = 4.5V$	2.1A

## Description

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

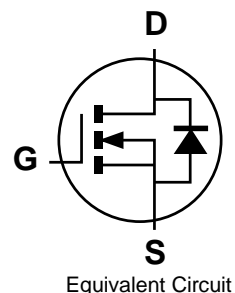
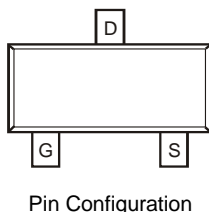
- DC-DC Converters
- Power Management Functions
- Analog Switch

## Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 **e3**
- Weight: 0.0072 grams (Approximate)

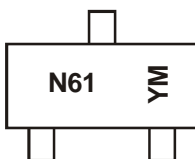


## Ordering Information (Note 4)

Part Number	Case	Packaging
DMN6140L-7	SOT23	3,000/Tape & Reel
DMN6140L-13	SOT23	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



N61 = Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: Y = 2011)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2011	2012	2013	2014	2015	2016	2017
Code	Y	Z	A	B	C	D	E

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	Steady State T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	1.6 1.2	A
	t < 10s T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	2.0 1.6	A
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	Steady State T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	2.3 1.8	A
	t < 10s T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	2.9 2.3	A
Maximum Continuous Body Diode Forward Current (Note 6)	I <sub>S</sub>	1.5	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	10	A

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P <sub>D</sub>	T <sub>A</sub> = +25°C	0.7
		T <sub>A</sub> = +70°C	0.4
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	Steady State	183
		t < 10s	115
Total Power Dissipation (Note 6)	P <sub>D</sub>	T <sub>A</sub> = +25°C	1.3
		T <sub>A</sub> = +70°C	0.8
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	Steady State	94
		t < 10s	61
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	39	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1	—	3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	92	140	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 1.8A
			115	170		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1.3A
Forward Transfer Admittance	Y <sub>fs</sub>	—	2.2	—	S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 1.8A
Diode Forward Voltage	V <sub>SD</sub>	—	0.75	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 0.45A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	315	—	pF	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	18	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	16	—		
Gate Resistnace	R <sub>g</sub>	—	0.65	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	8.6	—	nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 1.8A
Total Gate Charge (V <sub>GS</sub> = 5V)	Q <sub>g</sub>	—	4.1	—		
Gate-Source Charge	Q <sub>gs</sub>	—	1.0	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	1.7	—		
Turn-On Delay Time	t <sub>D(on)</sub>	—	2.6	—	ns	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 10V, R <sub>G</sub> = 6.0Ω, I <sub>D</sub> = 1.8A
Turn-On Rise Time	t <sub>r</sub>	—	3.6	—		
Turn-Off Delay Time	t <sub>D(off)</sub>	—	16.3	—		
Turn-Off Fall Time	t <sub>f</sub>	—	2.7	—		
Reverse Recovery Time	t <sub>rr</sub>	—	16.8	—	ns	I <sub>F</sub> = 1.8A, di/dt = 100A/µs
Reverse Recovery Charge	Q <sub>rr</sub>	—	9.0	—	nC	

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1in. square copper plate.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

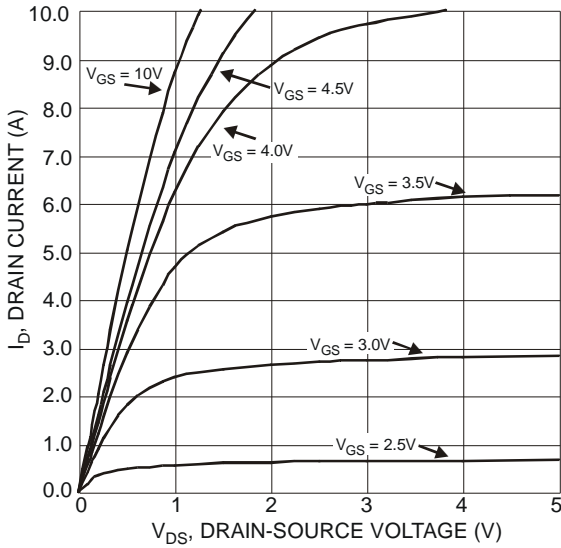


Figure 1 Typical Output Characteristic

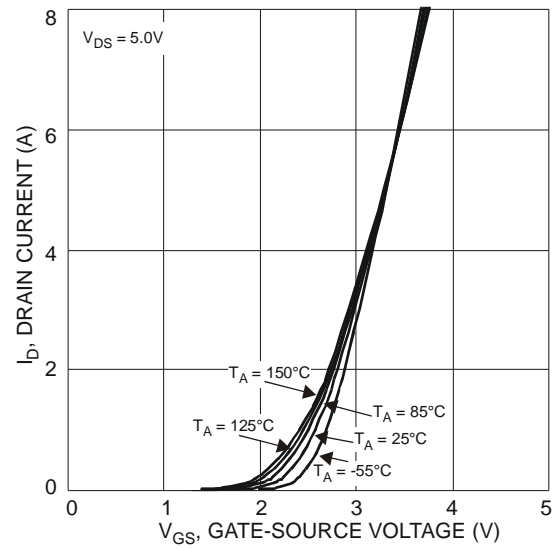


Figure 2 Typical Transfer Characteristics

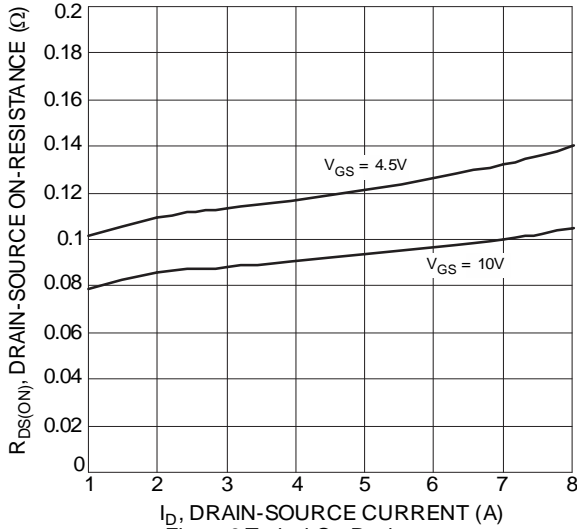


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

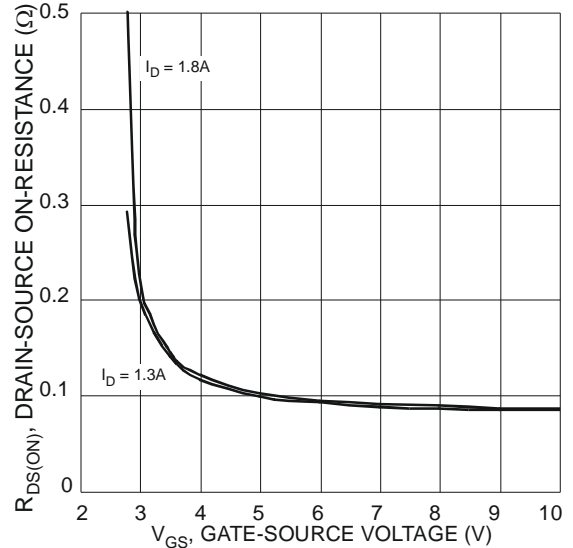


Figure 4 Typical Drain-Source On Resistance vs. Gate-Source Voltage

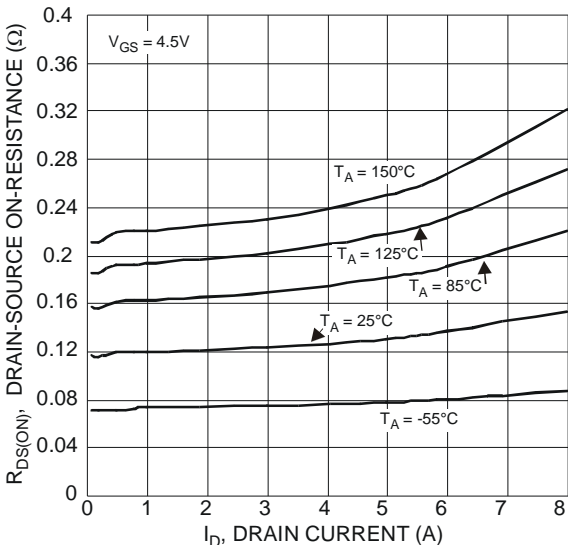


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

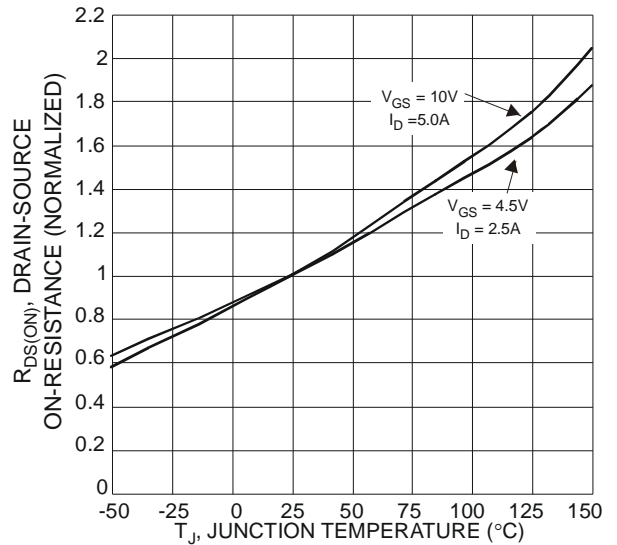


Figure 6 On-Resistance Variation with Temperature

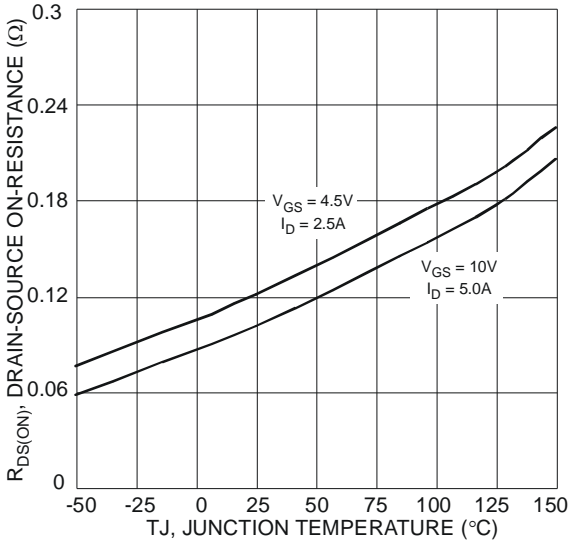


Figure 7 On-Resistance Variation with Temperature

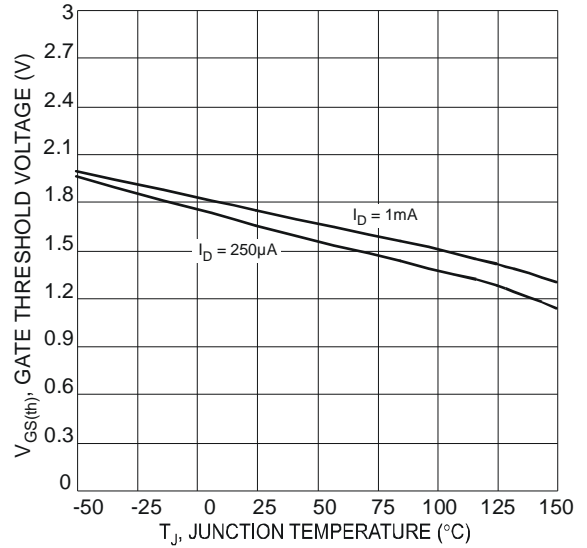


Figure 8 Gate Threshold Variation vs. Ambient Temperature

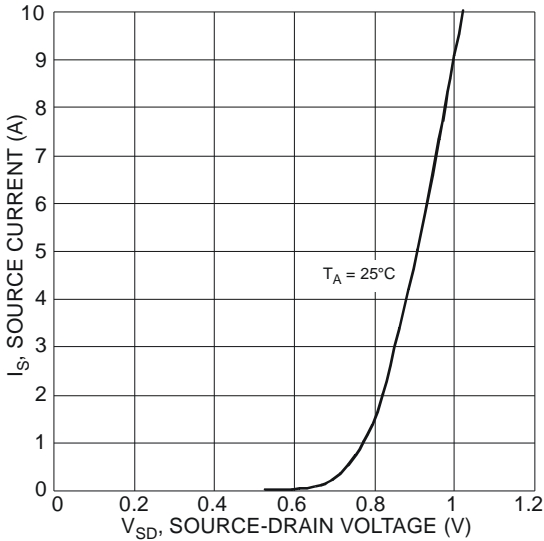


Figure 9 Diode Forward Voltage vs. Current

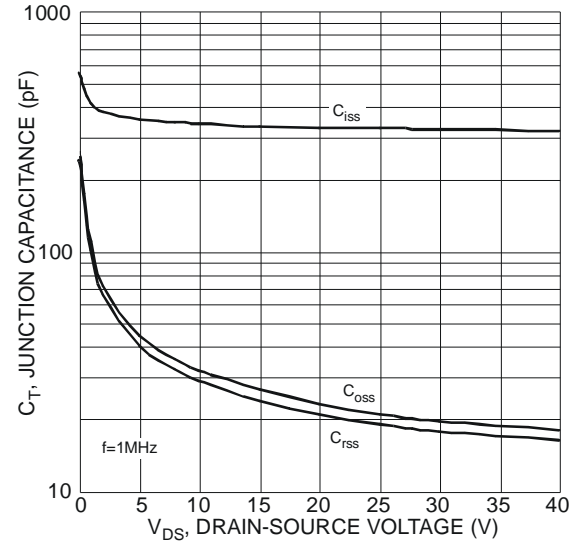


Figure 10 Typical Junction Capacitance

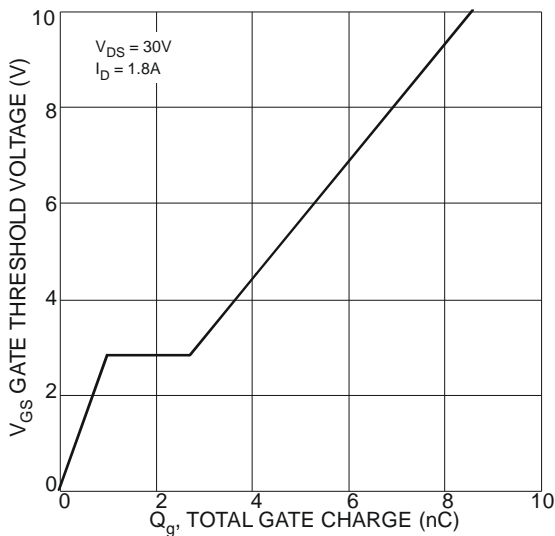


Figure 11 Gate Charge

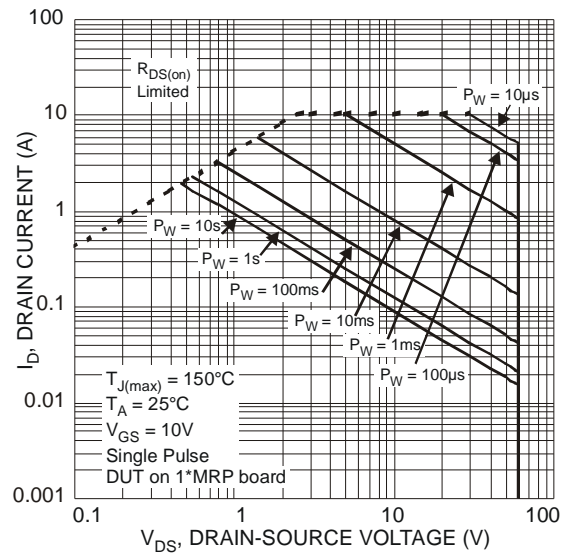
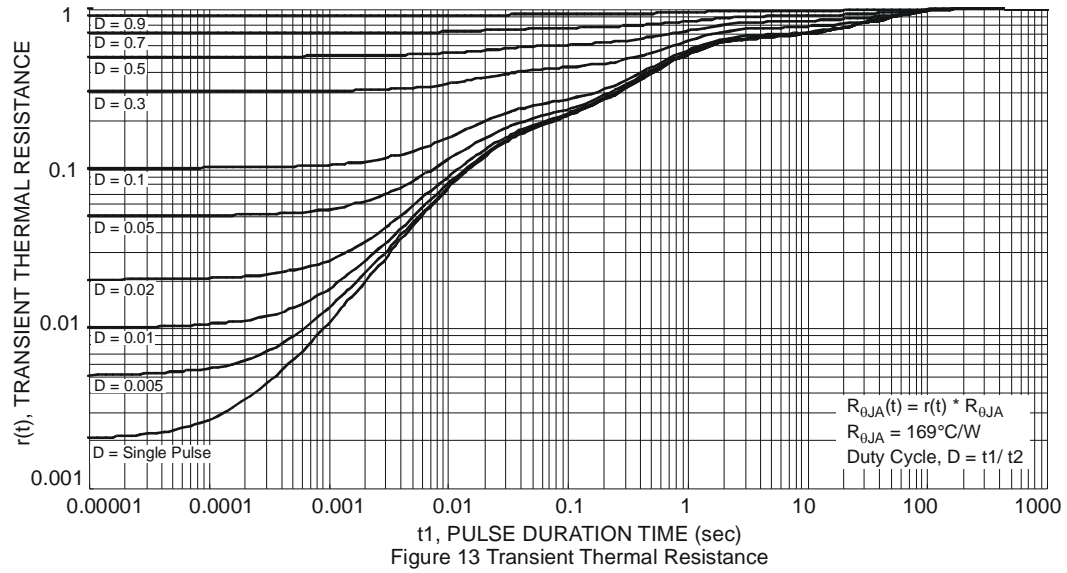
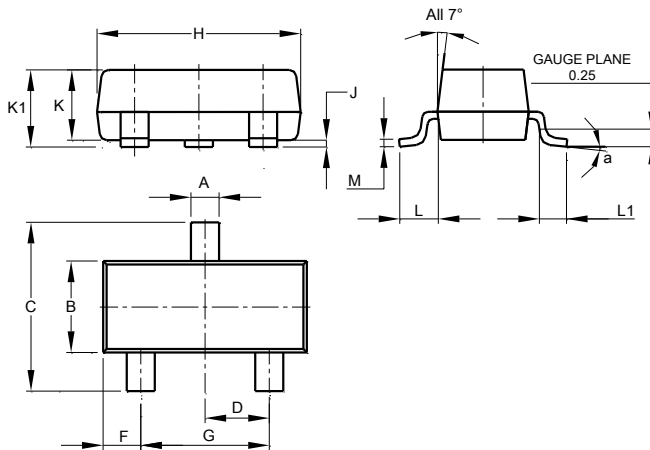


Figure 12 SOA, Safe Operation Area



**Package Outline Dimensions**

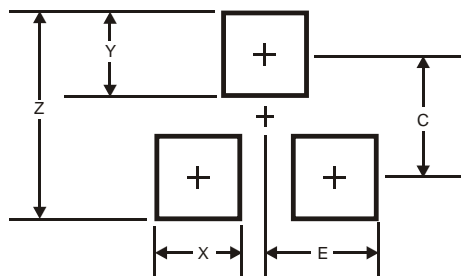
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
α	8°		
All Dimensions in mm			

**Suggested Pad Layout**

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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